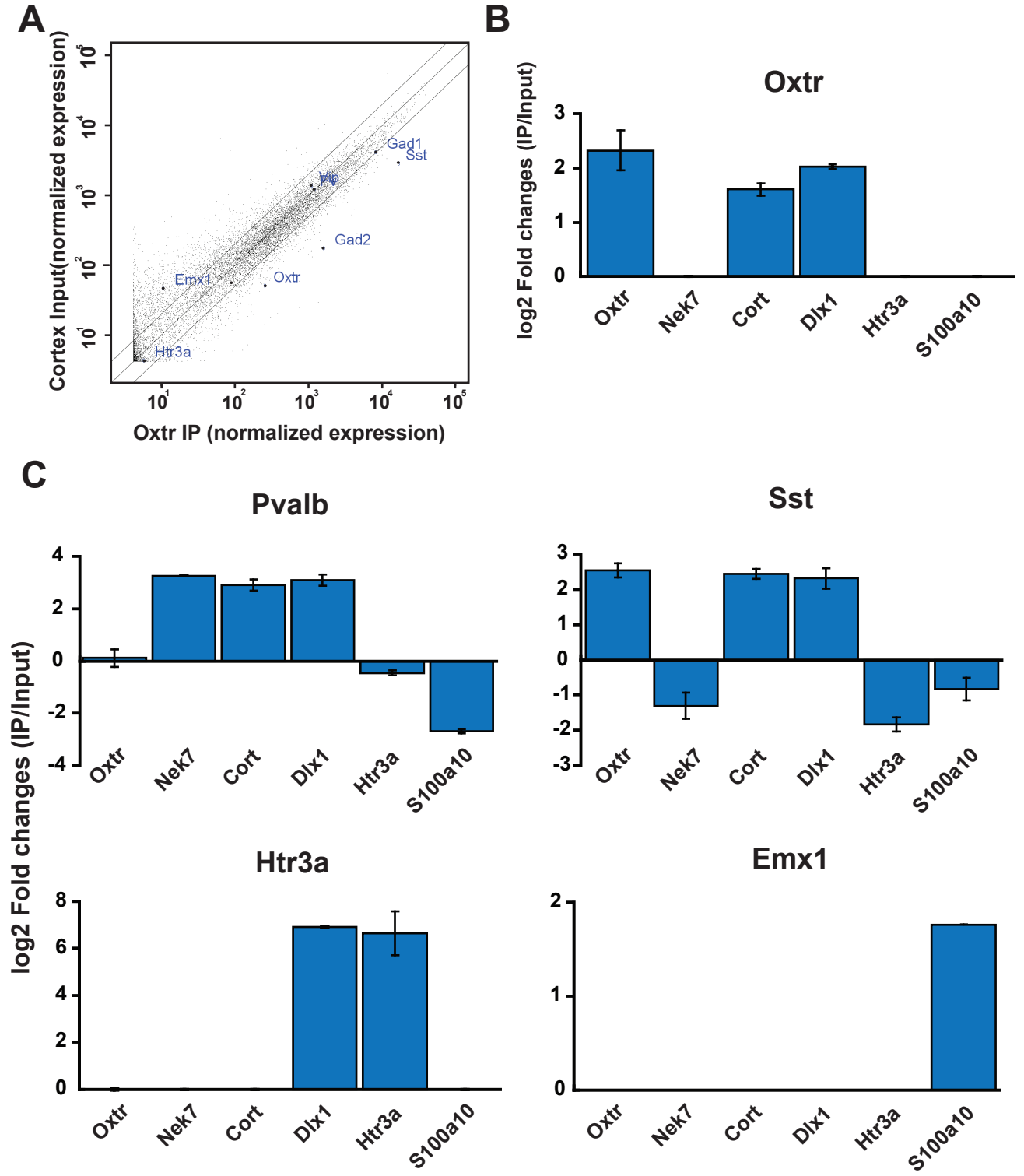
Figure S4


A
Cortex Input(normalized expression)
Oxtr IP (normalized expression)
Gad1
Sst
Vip
Gad2
Emx1
Oxtr
Htr3a
B
Oxtr
log2 Fold changes (IP/Input)
Oxtr
Nek7
Cort
Dlx1
Htr3a
S100a10
C
Pvalb
Sst
Htr3a
Emx1
log2 Fold changes (IP/Input)